

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claim 1 (original): A method for generating a random value, said method comprising:

monitoring a signal obtained from a communication channel, said signal including additive noise;

sampling said signal to generate a random value; and
storing said random value.

Claim 2 (original): The method of claim 1 further comprising:
using said random value as input to a cryptographic key generation process.

Claim 3 (original): The method of claim 1 wherein sampling comprises:
sampling at times determined by output of a linear feedback shift register.

Claim 4 (original): The method of claim 1 wherein monitoring comprises
monitoring a digital signal represented by multiple bits.

Claim 5 (original): The method of claim 4 further comprising:
reordering said multiple bits prior to sampling.

Claim 6 (currently amended): The method of claim 4 wherein said digital signal
comprises output of a [[digital to]] analog to digital converter.

Claim 7 (original): Apparatus for generating a random value, said apparatus
comprising:

means for monitoring a signal obtained from a communication channel, said
signal including additive noise;

means for sampling said signal to generate a random value; and
means for storing said random value.

Claim 8 (original): The apparatus of claim 7 further comprising:
means for using said random value as input to a cryptographic key generation
process.

Claim 9 (original): The apparatus of claim 7 wherein said sampling means
comprises:
means for sampling at times determined by output of a linear feedback shift
register.

Claim 10 (original): The apparatus of claim 7 wherein said means for
monitoring comprises means for monitoring a digital signal represented by multiple bits.

Claim 11 (original): The apparatus of claim 10 further comprising:
means for reordering said multiple bits prior to sampling.

Claim 12 (currently amended): The apparatus of claim 10 wherein said
digital signal comprises output of a [[digital]] to analog to digital converter.

Claim 13 (original): Apparatus for generating a random value, said apparatus
comprising:
a monitoring circuit that monitors a signal derived from a communication channel
output; and
a register that stores a random value generated from said signal.

Claim 14 (original): The apparatus of claim 13 further comprising:
a sampler that samples said signal to generate said random value.

Claim 15 (original): The apparatus of claim 14 further comprising:
a linear feedback shift register that controls sampling times of said samples.

Claim 16 (original): The apparatus of claim 14 wherein said signal comprises a
digital signal.

Claim 17 (original): The apparatus of claim 13 wherein said digital signal is
represented by multiple bits and further comprising:
a bit reordering stage that reorders said multiple bits to generate said random
value.

Claim 18 (original): The apparatus of claim 16 wherein said digital signal is
obtained from output of an analog to digital converter.

Claim 19 (new): The method of claim 1 wherein the signal further includes a
modulation signal, and the additive noise is Additive White Gaussian Noise.

Claim 20 (new): The method of claim 1 wherein the communication channel
is a wireless communication channel of a communication network, and the signal is arranged to
include data.